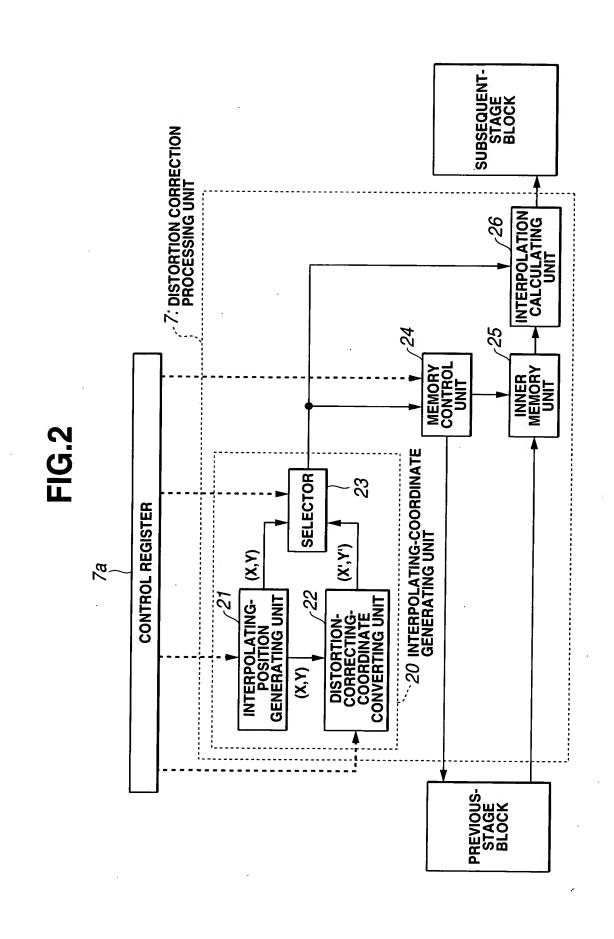
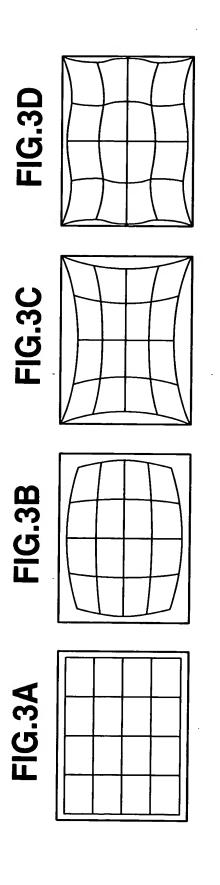
MEMORY CARD, ETC. PROCESSING UNIT 9 2ND DATA-ORDER CONVERTING UNIT  $\boldsymbol{\omega}$ 11: BUS DISTORTION CORRECTION PROCESSING UNIT PROCESSING UNIT **FRAME MEMORY** 6 1ST DATA-ORDER CONVERTING UNIT 5 CPU PROCESSING UNIT CCD

. 9





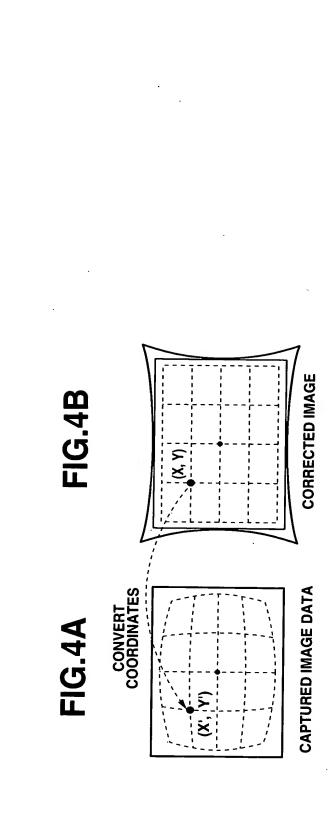


FIG.5

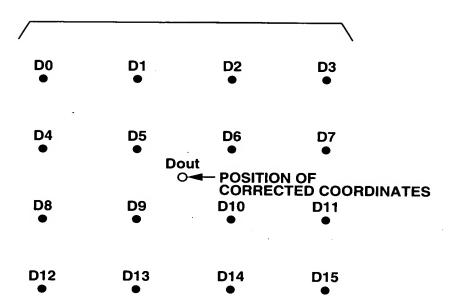


FIG.6

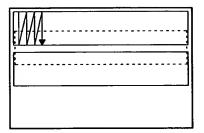


FIG.7A

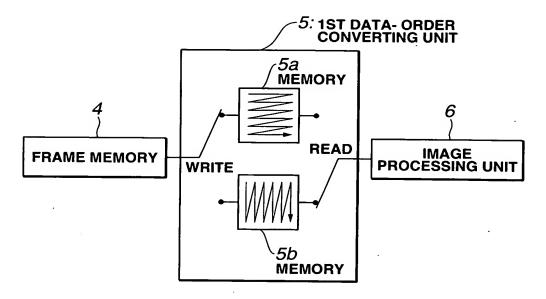


FIG.7B

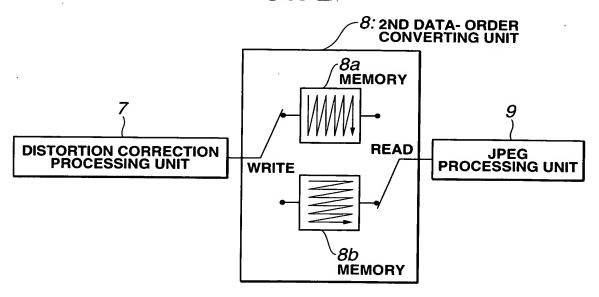


FIG.8

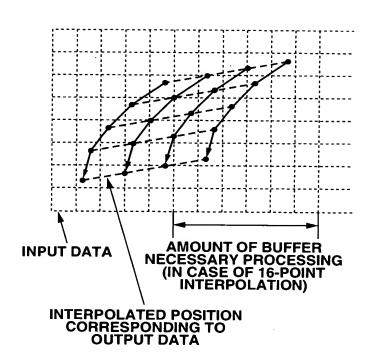
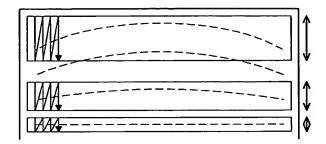
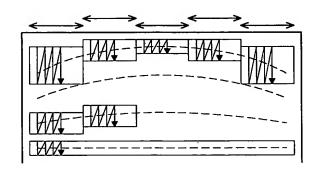
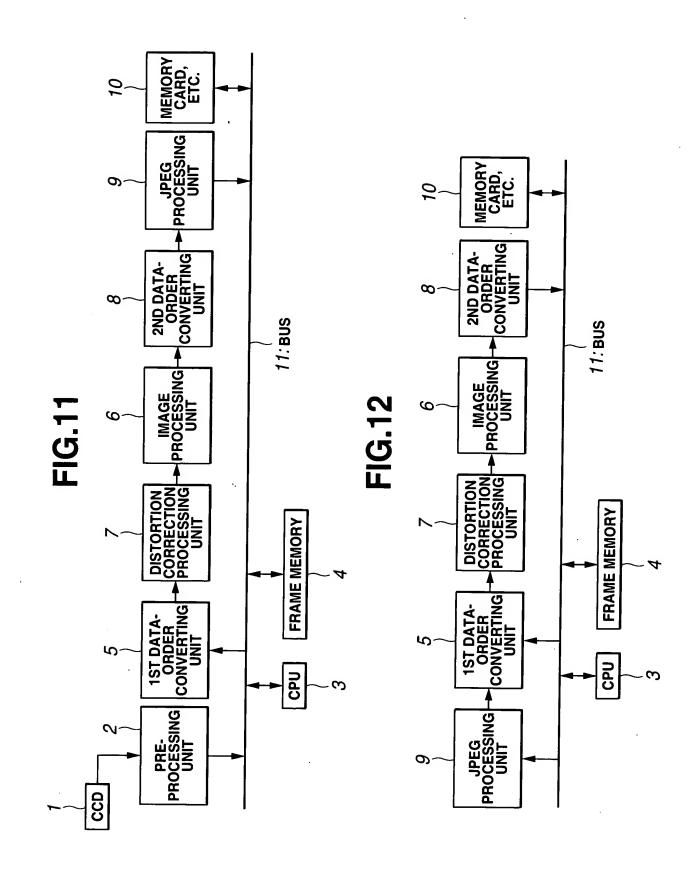


FIG.9



**FIG.10** 





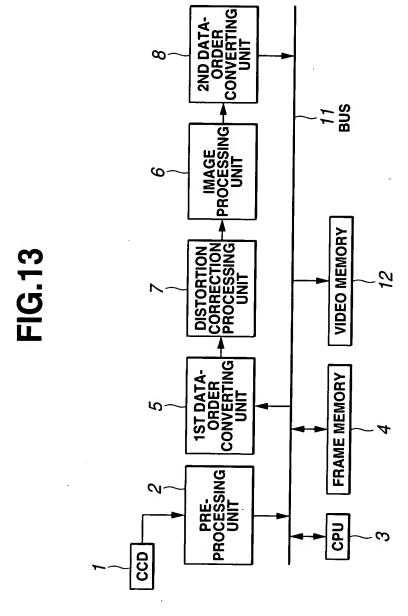
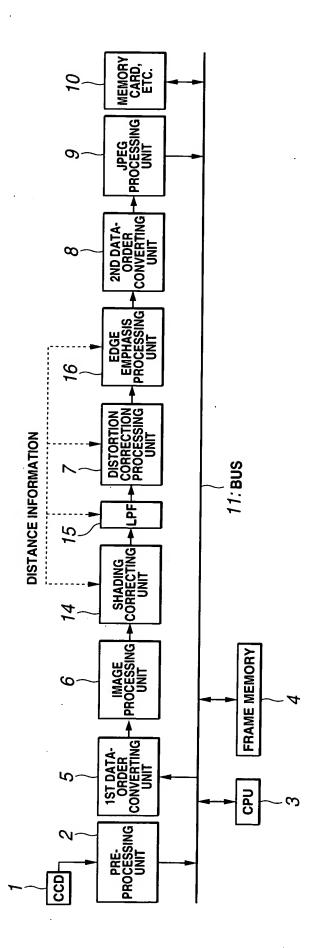
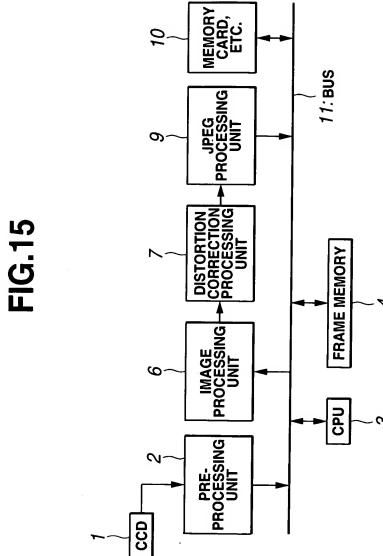


FIG.14





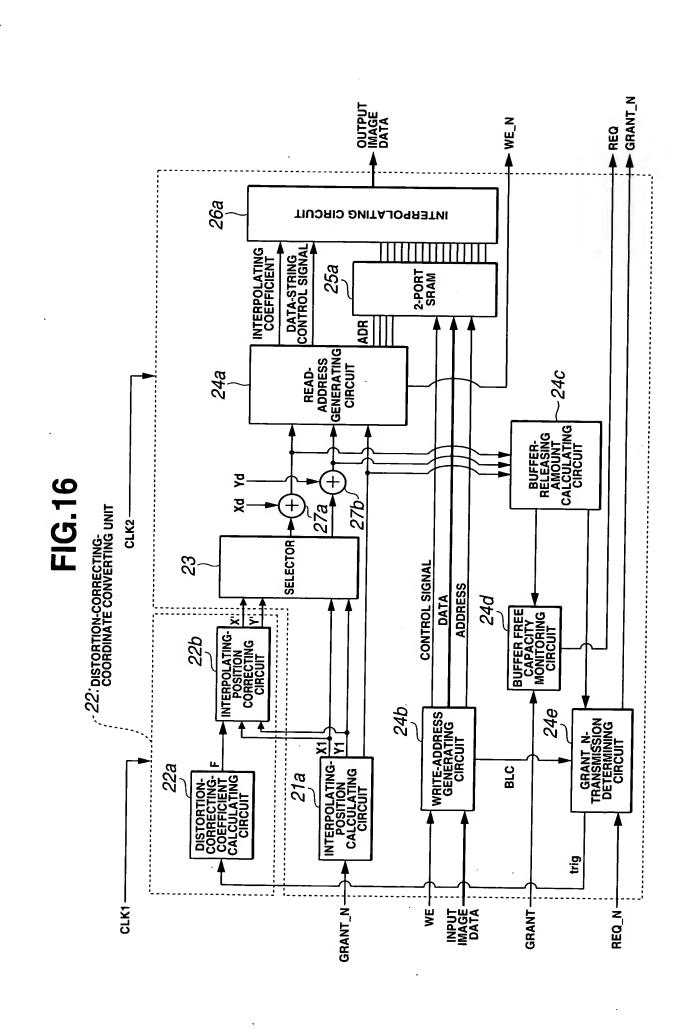
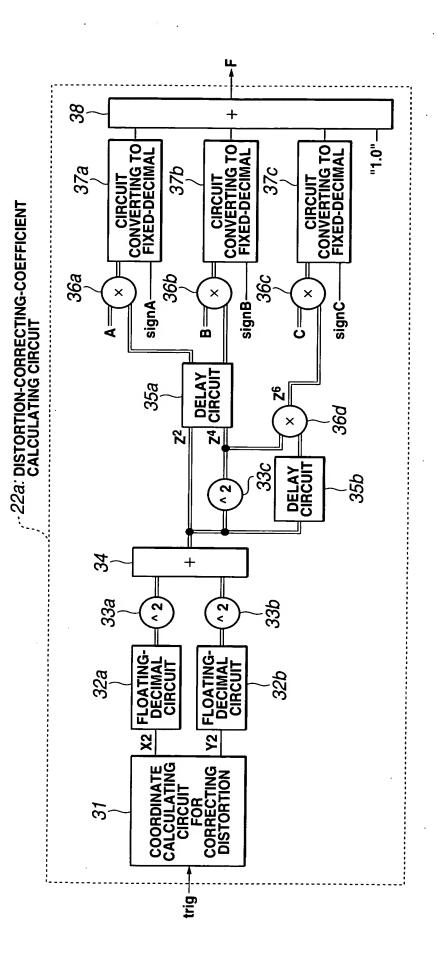
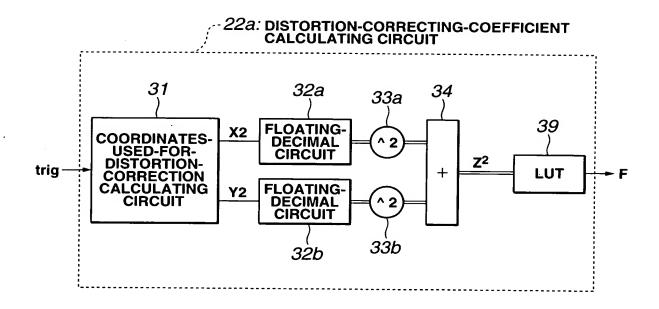


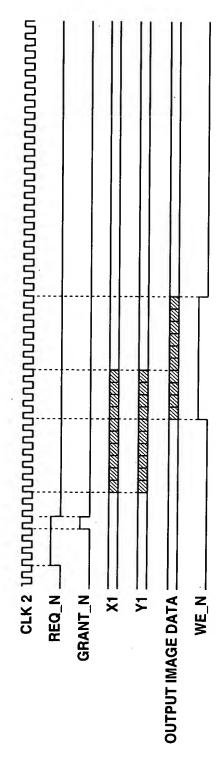
FIG.17



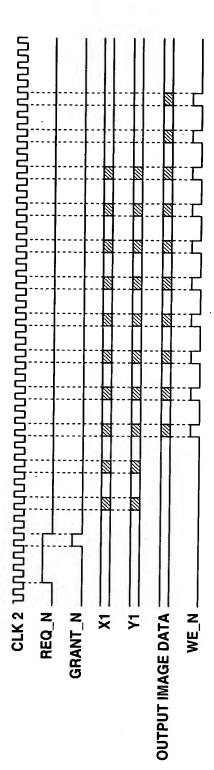
**FIG.18** 



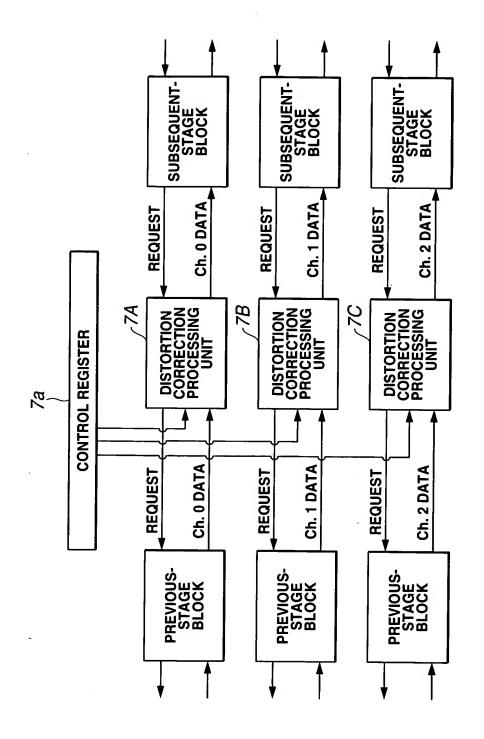
**FIG.19** 



**FIG.20** 



**FIG.21** 



**FIG.22** 

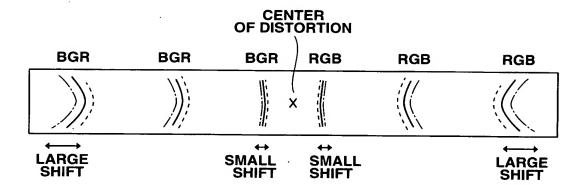
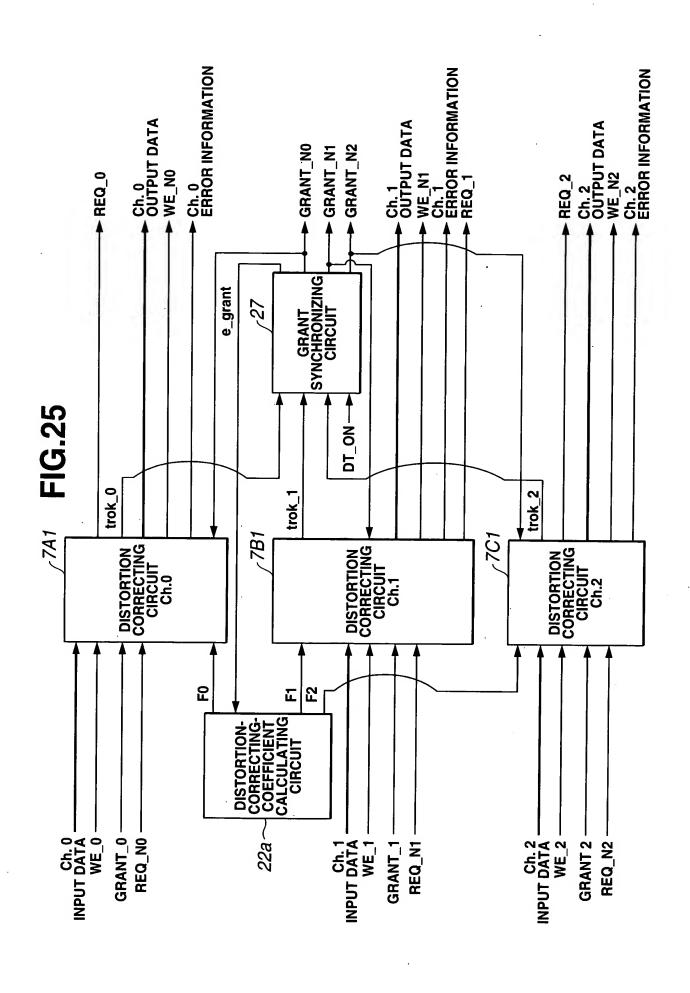
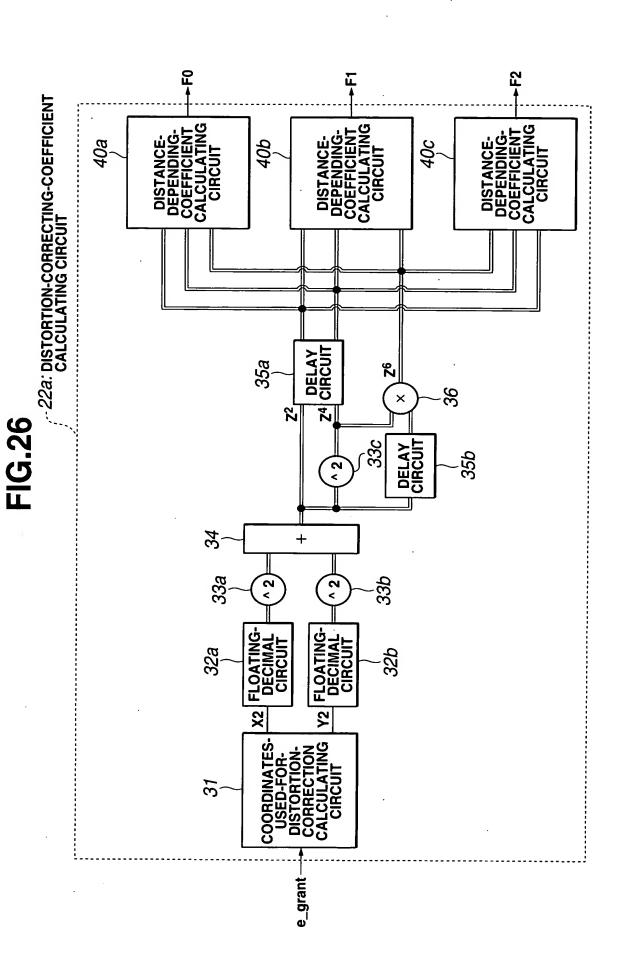


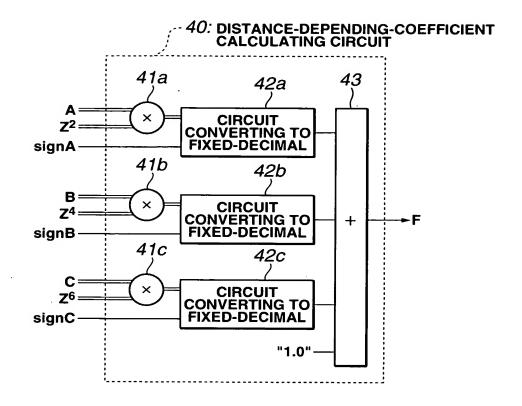
FIG.23A R FIG.23B G FIG.23C B

FIG.24A R FIG.24B G FIG.24C B

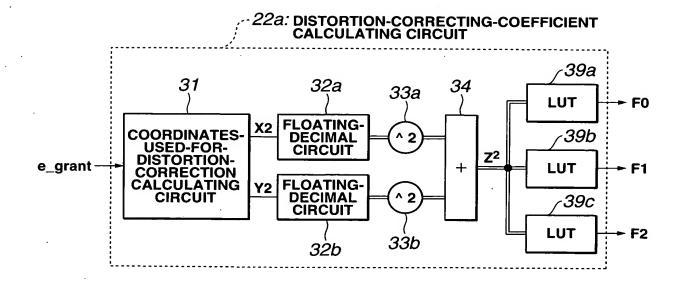


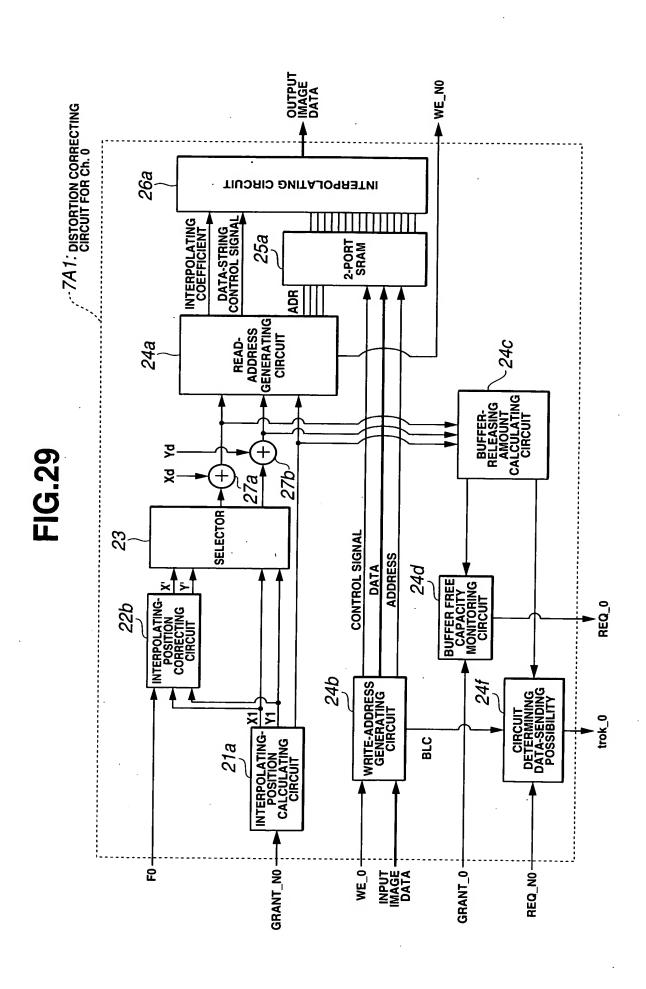


## **FIG.27**

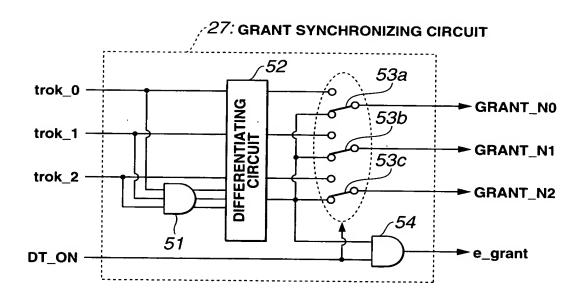


**FIG.28** 





**FIG.30** 



## **FIG.31**

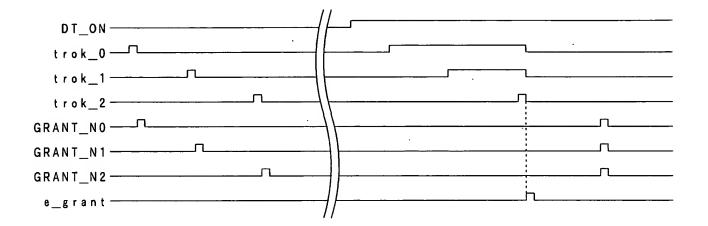
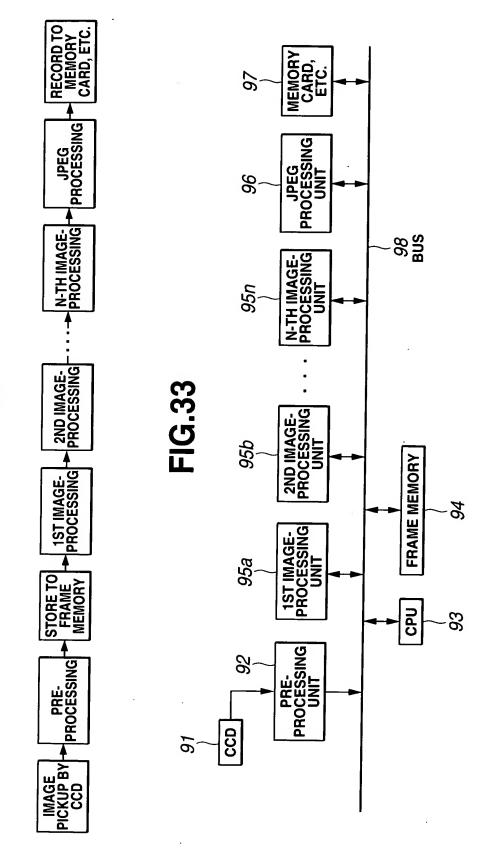


FIG.32



**FIG.34** 

